

wherein said frame composition identification section is composed to perform serially said identification processing according to a rate based on a transmission rate of said transmission frame.

34. An apparatus comprising:

a pointer processing section for processing a pointer contained in a transmission frame; and

a frame identification section for identifying a frame size of said transmission frame based on pointer processing result of said pointer processing section and for providing such identification result to said pointer processing section.

#### REMARKS

Claims 16-34 are pending in the present application.

#### NEWLY ADDED CLAIMS

Newly added claim 16 is directed to a reception pointer processing apparatus, including a frame composition identification section for identifying a concatenation state of a transmission frame transmitted by an SDH transmission system based on pointer processing result of a pointer processing section and for providing such identification result to said pointer processing section. Bleickardt et al. fail to teach or suggest this feature as described in claim 16. Rather, Bleickhardt et al. teach an evaluator 507 for detecting J1 bytes and stuffing indicator bytes.


Newly added claim 17-18 depend from claim 16 and should be allowed for the reasons discussed above and for the additional features recited therein.

Accordingly, as the cited art fails to teach the presently claimed invention and benefits derived therefrom, it is respectfully requested that the Examiner withdraw all rejections under 35 U.S.C. §102(b).

**CLOSING**

Any fee due with this paper, not fully covered by an enclosed check, may be charged on Deposit Account 08-1634.

Respectfully submitted,

  
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